

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary



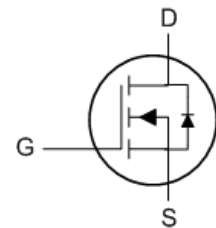
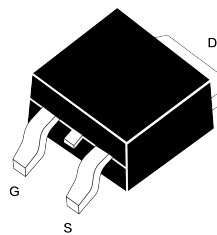
BVDSS	RDS(on)	ID
60V	60 mΩ	10A

Description

The JH10N06 is the high cell density trenched N-ch MOSFETs, which provide excellent RDS(on) and gate charge for most of the synchronous buck converter applications.

The JH10N06 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

TO-252 Pin Configuration



■ Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise noted)

Parameter		Symbol	Limit	Unit
Drain-source Voltage		V_{DS}	60	V
Gate-source Voltage		V_{GS}	± 20	V
Drain Current	$T_A=25^{\circ}\text{C}$	I_D	10	A
	$T_A=100^{\circ}\text{C}$		8	
Pulsed Drain Current ^A		I_{DM}	18	A
Total Power Dissipation ^B	$T_A=25^{\circ}\text{C}$	P_D	1.2	W
	$T_A=100^{\circ}\text{C}$		0.45	
Junction and Storage Temperature Range		T_J, T_{STG}	-55~+150	$^{\circ}\text{C}$

■ Thermal resistance

Parameter		Symbol	Typ	Max	Units
Thermal Resistance Junction-to-Ambient ^C	Steady-State	$R_{\theta JA}$	85	105	$^{\circ}\text{C/W}$

■ Electrical Characteristics (T_J=25°C unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1	μA
		V _{DS} =60V, V _{GS} =0V, T _J =150°C	-	-	100	
Gate-Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} =0V	-	-	±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =250μA	0.9	1.35	2	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =3A	-	60	82	mΩ
		V _{GS} =4.5V, I _D =3A	-	68	92	
Diode Forward Voltage	V _{SD}	I _S =3A, V _{GS} =0V	-	0.85	1.2	V
Gate resistance	R _G	f=1MHz, Open drain	-	2	-	Ω
Maximum Body-Diode Continuous Current	I _S		-	-	10	A
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{DS} =30V, V _{GS} =0V, f=1MHz	-	500	-	pF
Output Capacitance	C _{oss}		-	28	-	
Reverse Transfer Capacitance	C _{rss}		-	22	-	
Switching Parameters						
Total Gate Charge	Q _g	V _{GS} =10V, V _{DS} =30V, I _D =3A	-	10	-	nC
Gate-Source Charge	Q _{gs}		-	1.7	-	
Gate-Drain Charge	Q _{gd}		-	2.1	-	
Reverse Recovery Charge	Q _{rr}	I _F =3A, di/dt=100A/us	-	7	-	nC
Reverse Recovery Time	t _{rr}		-	33	-	ns
Turn-on Delay Time	t _{D(on)}	V _{GS} =10V, V _{DD} =30V, R _L =20Ω R _{GEN} =3Ω	-	3.6	-	ns
Turn-on Rise Time	t _r		-	17.6	-	
Turn-off Delay Time	t _{D(off)}		-	13	-	
Turn-off fall Time	t _f		-	23	-	

A. Repetitive rating; pulse width limited by max. junction temperature.

B. P_d is based on max. junction temperature, using junction-case thermal resistance.

C. The value of R_{θJA} is measured with the device mounted on the minimum recommend pad size, in the still air environment with T_A =25°C. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.

Typical Electrical and Thermal Characteristics Diagrams

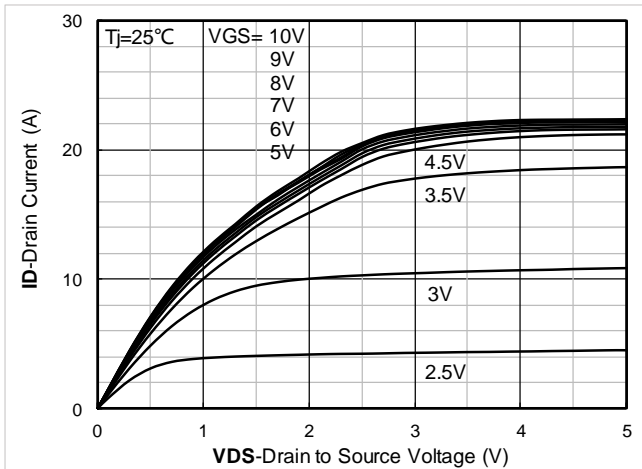


Figure 1. Output Characteristics

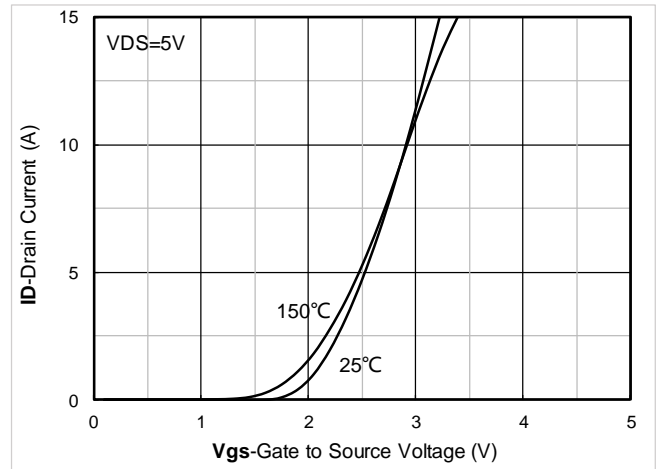


Figure 2. Transfer Characteristics

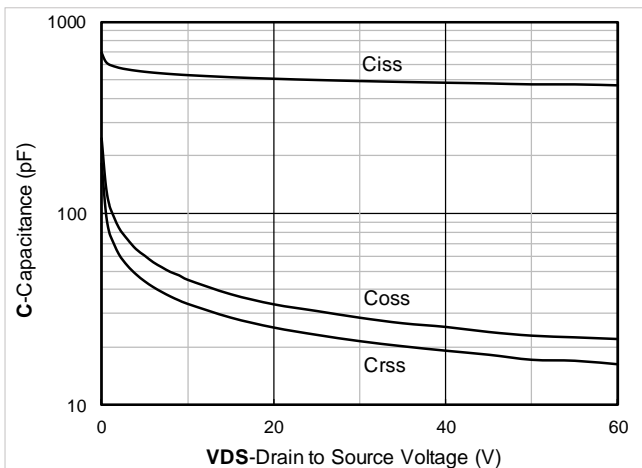


Figure 3. Capacitance Characteristics

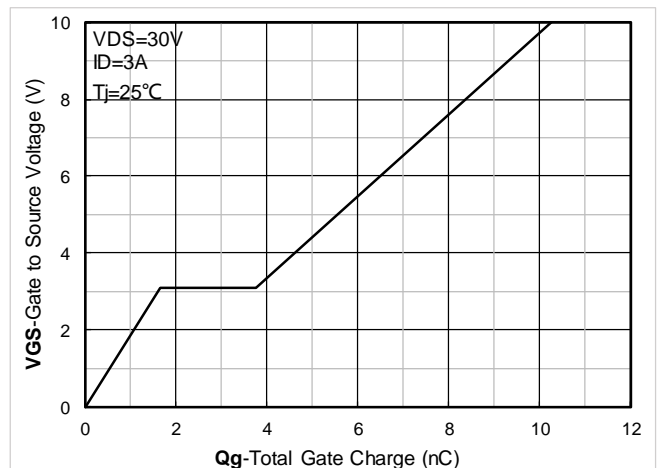


Figure 4. Gate Charge

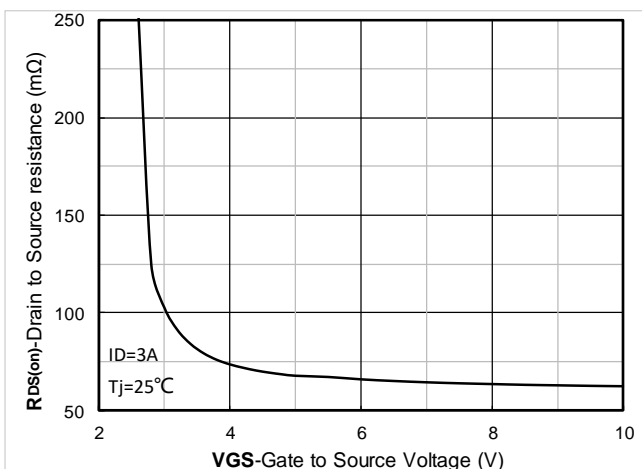


Figure 5. On-Resistance vs Gate to Source Voltage

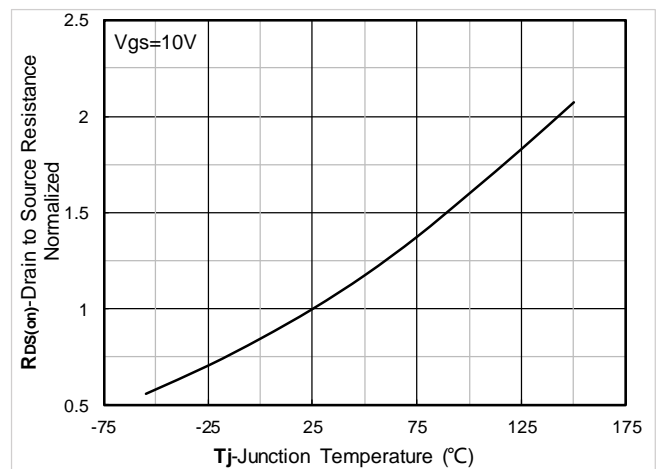


Figure 6. Normalized On-Resistance

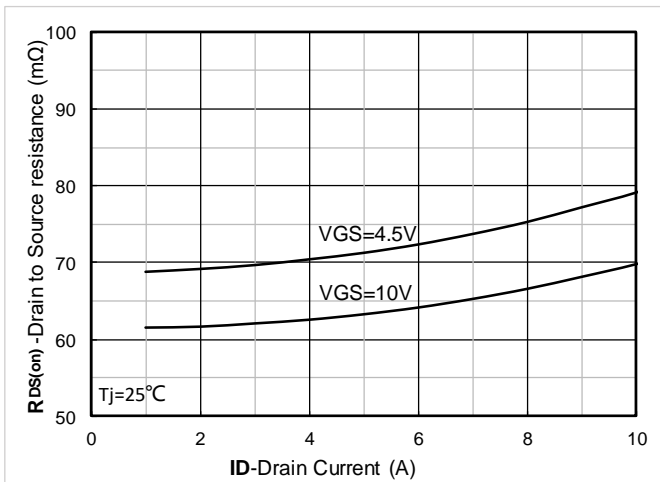


Figure 7. RDS(on) VS Drain Current

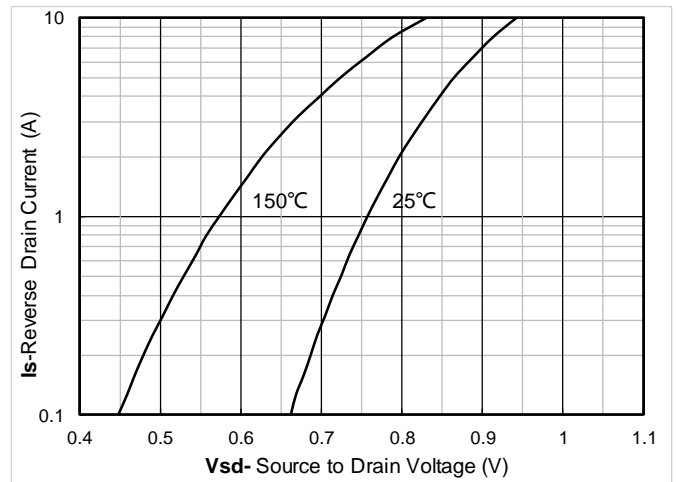


Figure 8. Forward characteristics of reverse diode

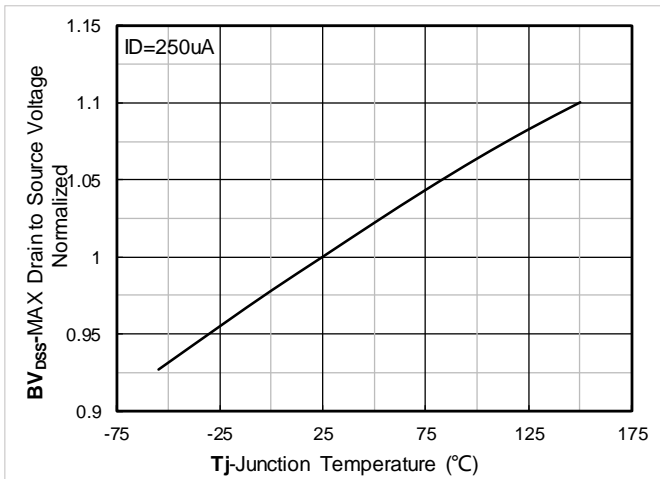


Figure 9. Normalized breakdown voltage

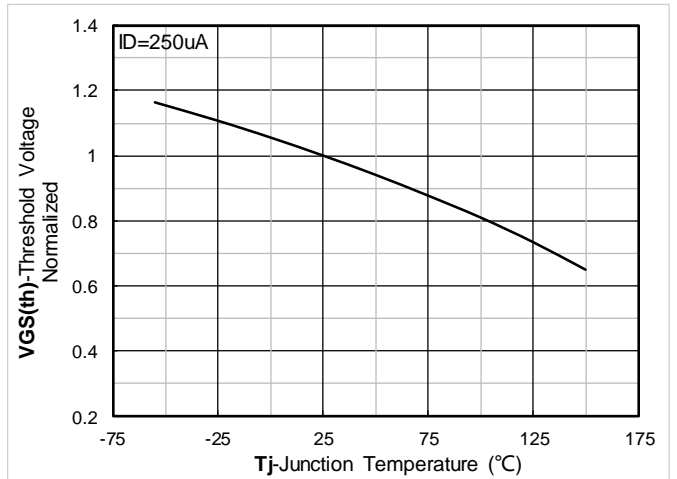


Figure 10. Normalized Threshold voltage

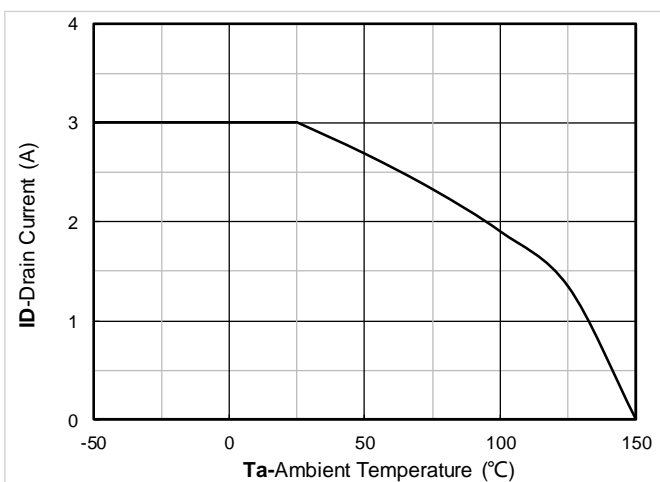


Figure 11. Current dissipation

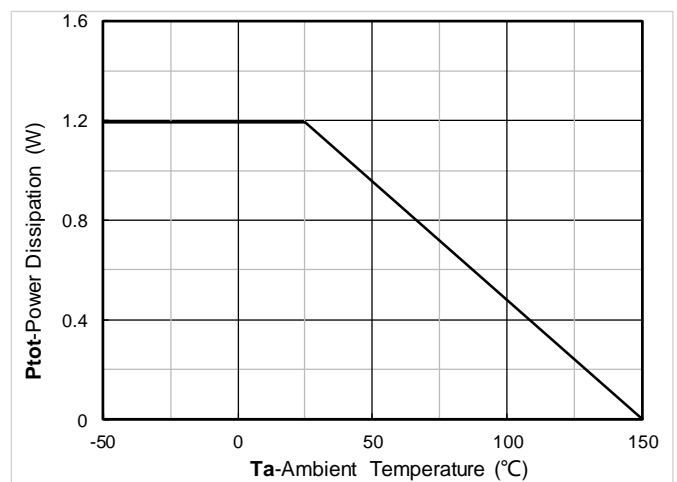


Figure 12. Power dissipation

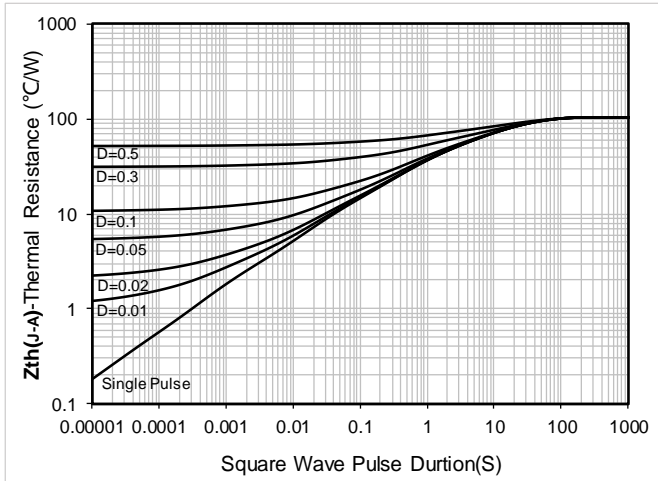


Figure 13. Maximum Transient Thermal Impedance

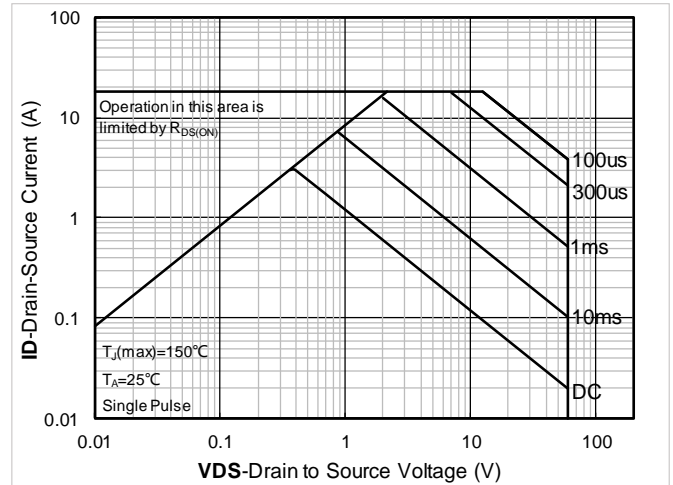


Figure 14. Safe Operation Area

■ Test Circuits & Waveforms

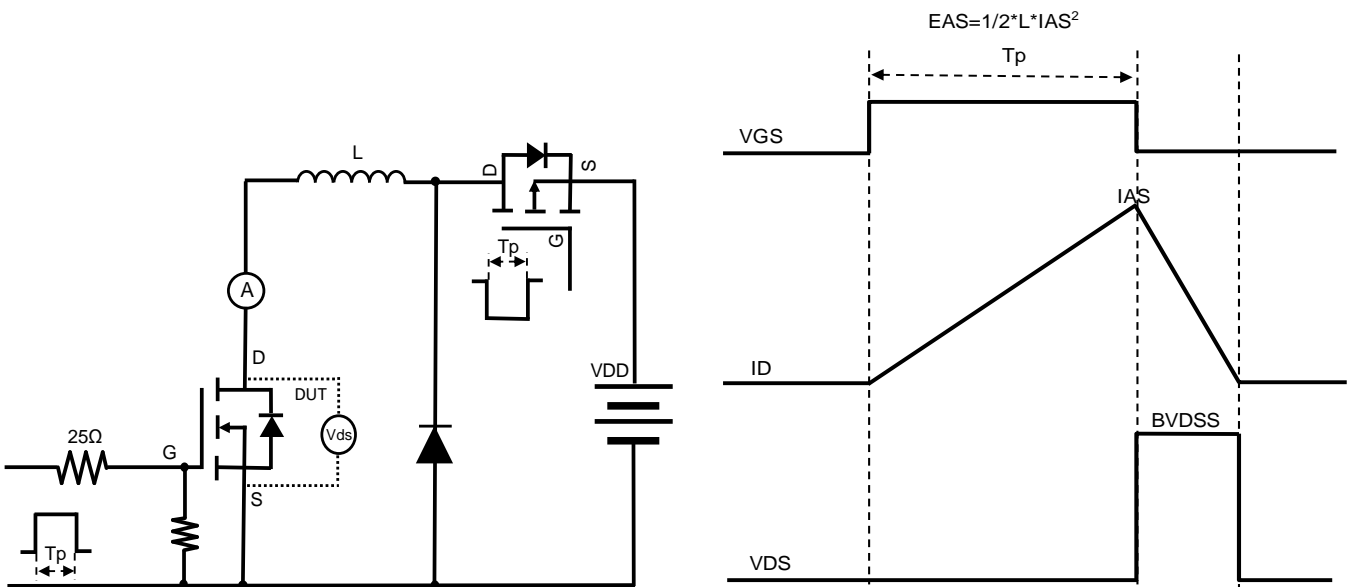


Figure A. Unclamped Inductive Switching (UIS) Test Circuit & Waveform

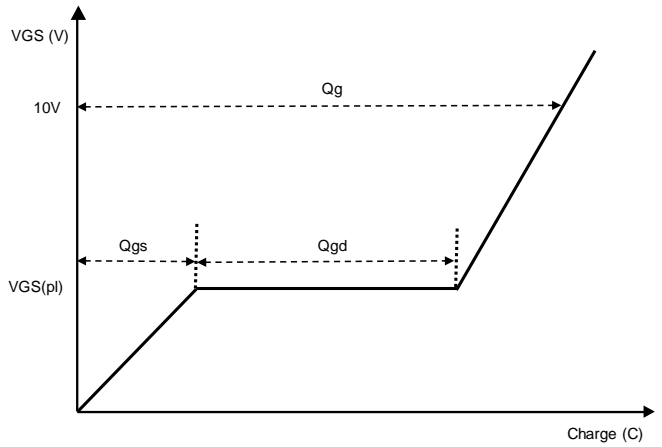
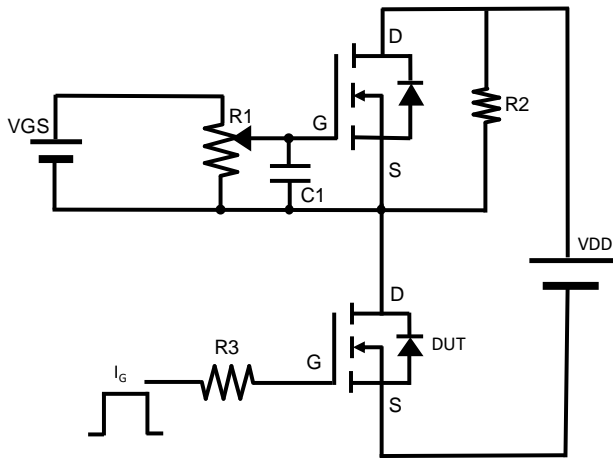


Figure B. Gate Charge Test Circuit & Waveform

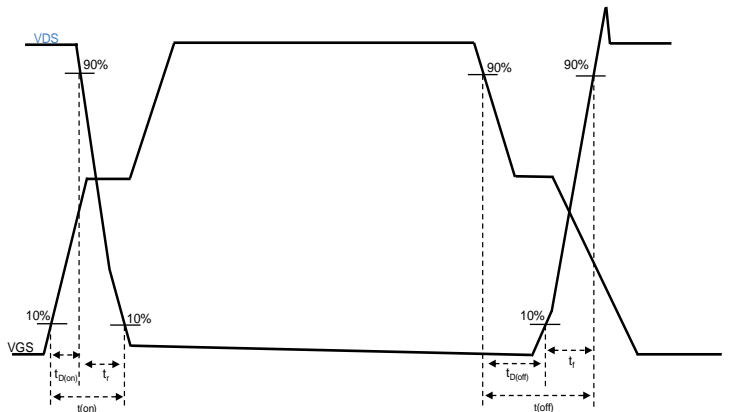
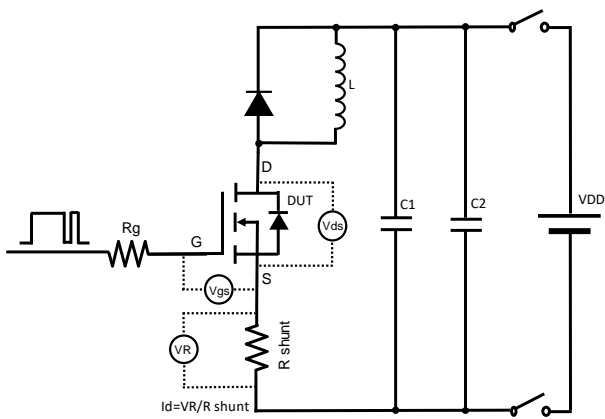


Figure C. Resistive Switching Test Circuit & Waveform

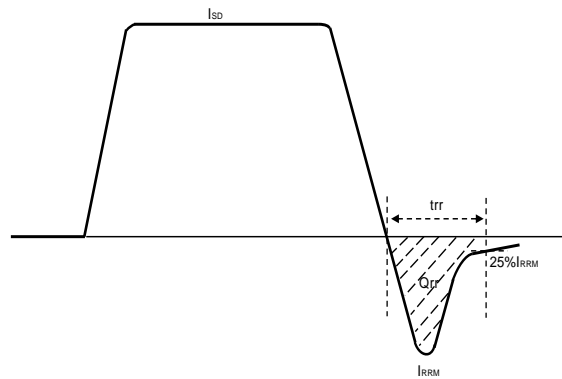
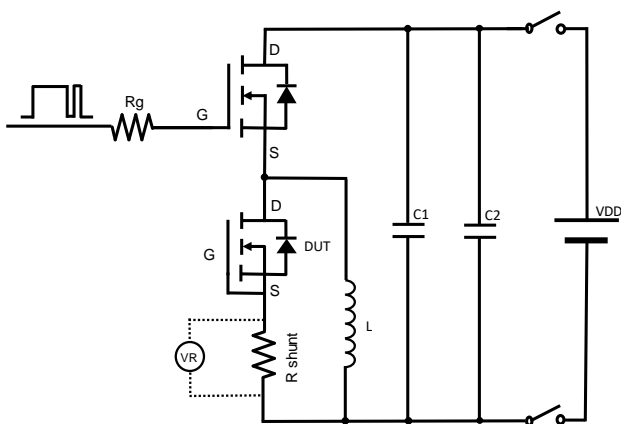
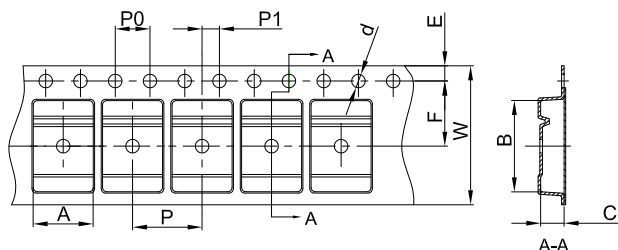


Figure D. Diode Recovery Test Circuit & Waveform

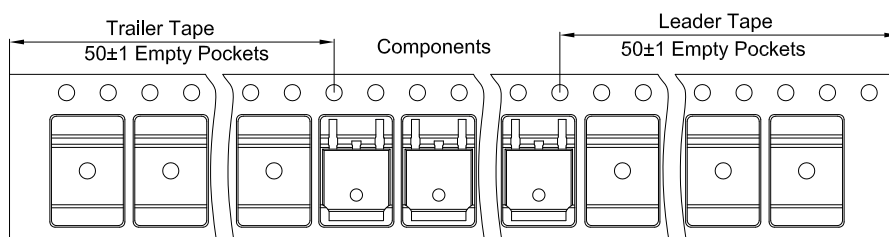
TO-252-2L Tape and Reel

TO-252 Embossed Carrier Tape

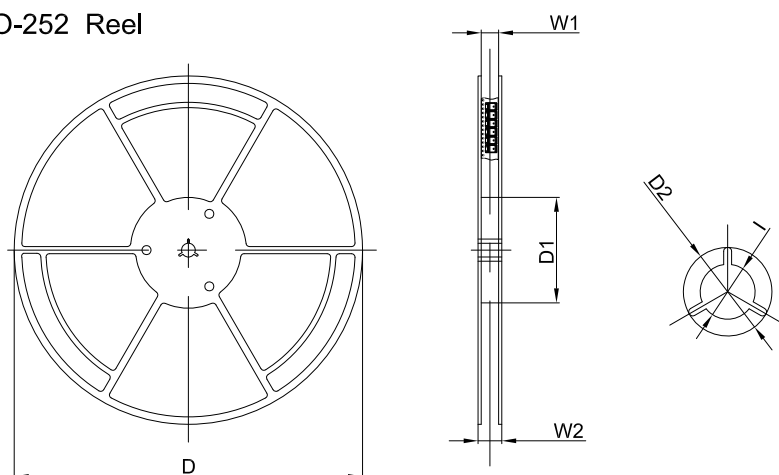


Dimensions are in millimeter										
Pkg type	A	B	C	d	E	F	P0	P	P1	W
TO-252	6.90	10.50	2.70	Ø1.55	1.75	7.50	4.00	8.00	2.00	16.00

TO-252 Tape Leader and Trailer

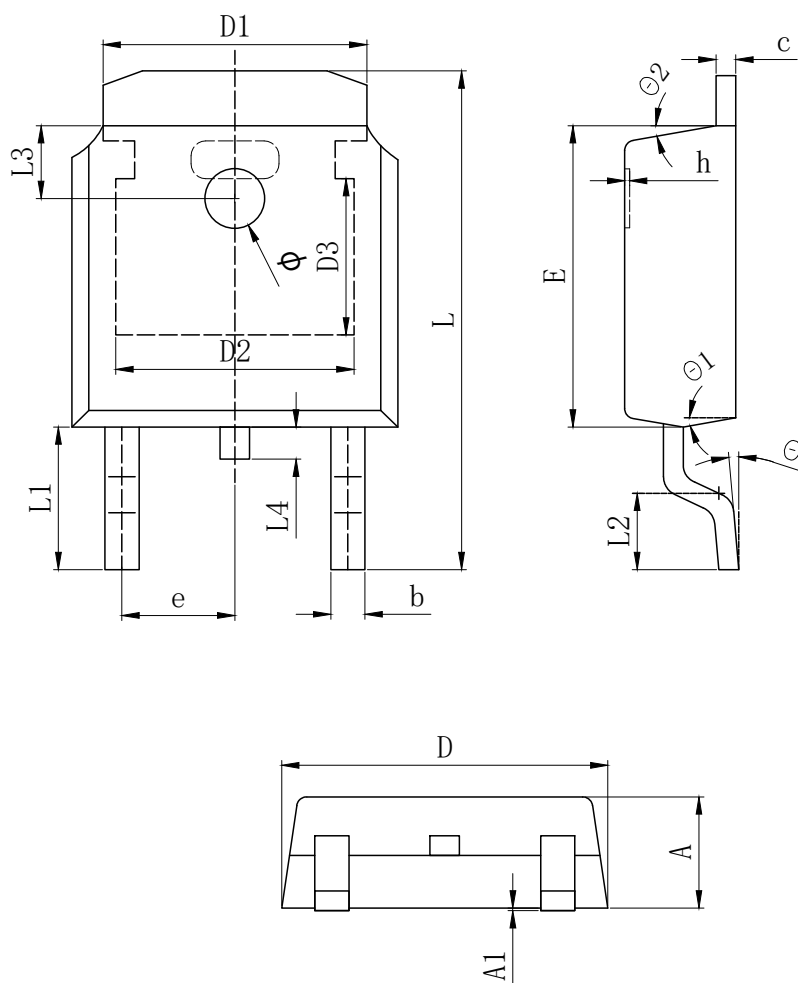


TO-252 Reel



Dimensions are in millimeter						
Reel Option	D	D1	D2	W1	W2	I
13" Dia	330.00	100.00	Ø21.00	16.40	21.00	Ø13.00

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
2,500 pcs	13inch	2,500 pcs	340×336×29	25,000 pcs	353×346×365	



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c (电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ 1	9° TYP		
θ 2	9° TYP		

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