

- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

Product Summary

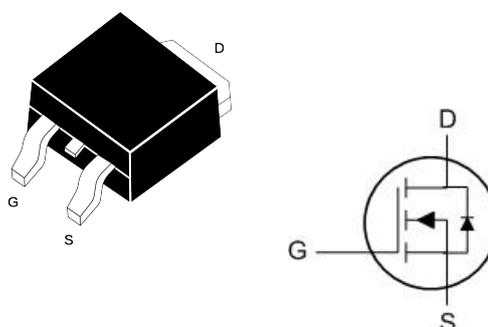


BVDSS	RDS(on)	ID
30V	1.6mΩ	160A

Description

The JH160N03 is the high cell density trenched N-ch MOSFETs, which provide excellent RDS(on) and gate charge for most of the synchronous buck converter applications. The JH160N03 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

TO-252 Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, V_{GS} @ 10V ^{1,6}	160	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, V_{GS} @ 10V ^{1,6}	80	A
I_{DM}	Pulsed Drain Current ²	450	A
EAS	Single Pulse Avalanche Energy ³	580	mJ
I_{AS}	Avalanche Current	60	A
$P_D@T_C=25^\circ C$	Total Power Dissipation ⁴	87	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	2.1	$^\circ C/W$

Electrical characteristic ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
Off characteristics						
BV_{DSS}	Drain to source breakdown voltage	$V_{GS}=0V, I_D=250\mu A$	30			V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown voltage temperature coefficient	$I_D=250\mu A$, referenced to 25°C		0.02		$V/^\circ\text{C}$
I_{DSS}	Drain to source leakage current	$V_{DS}=30V, V_{GS}=0V$			1	μA
		$V_{DS}=24V, T_J=125^\circ\text{C}$			50	μA
I_{GSS}	Gate to source leakage current, forward	$V_{GS}=20V, V_{DS}=0V$			100	nA
	Gate to source leakage current, reverse	$V_{GS}=-20V, V_{DS}=0V$			-100	nA
On characteristics						
$V_{GS(TH)}$	Gate threshold voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	1.2		2.4	V
$R_{DS(ON)}$	Drain to source on state resistance	$V_{GS}=4.5V, I_D=30A, T_J=25^\circ\text{C}$		2.2	4.8	$m\Omega$
		$V_{GS}=10V, I_D=30A, T_J=25^\circ\text{C}$		1.6	2.9	$m\Omega$
		$V_{GS}=10V, I_D=30A, T_J=125^\circ\text{C}$		2.5		$m\Omega$
G_{fs}	Forward transconductance	$V_{DS}=5V, I_D=30A$		73		S
Dynamic characteristics						
C_{iss}	Input capacitance	$V_{GS}=0V, V_{DS}=15V, f=1\text{MHz}$		6272		pF
C_{oss}	Output capacitance			1022		
C_{rss}	Reverse transfer capacitance			718		
$t_{d(on)}$	Turn on delay time	$V_{DS}=15V, I_D=30A, R_G=4.7\Omega, V_{GS}=10V$ (note 4,5)		20		ns
t_r	Rising time			58		
$t_{d(off)}$	Turn off delay time			158		
t_f	Fall time			77		
Q_g	Total gate charge	$V_{DS}=24V, V_{GS}=10V, I_D=30A, I_G=5mA$ (note 4,5)		143		nC
Q_{gs}	Gate-source charge			17		
Q_{gd}	Gate-drain charge			43		
R_g	Gate resistance		$V_{DS}=0V$, Scan F mode		4.2	

Source to drain diode ratings characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_S	Continuous source current	Integral reverse p-n Junction diode in the MOSFET			160	A
I_{SM}	Pulsed source current				440	A
V_{SD}	Diode forward voltage drop.	$I_S=45A, V_{GS}=0V$			1.4	V
t_{rr}	Reverse recovery time	$I_S=30A, V_{GS}=0V, di_F/dt=100A/\mu s$		26		ns
Q_{rr}	Reverse recovery charge			10		nC

※. Notes

1. Repeattive rating : pulse width limited by junction temperature.
2. $L=0.5mH, I_{AS}=48A, V_{DD}=30V, R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$
3. $I_{SD} \leq 30A, di/dt = 100A/\mu s, V_{DD} \leq BV_{DSS}$, Starting $T_J=25^\circ\text{C}$
4. Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- 5.

N- Channel Typical Characteristics

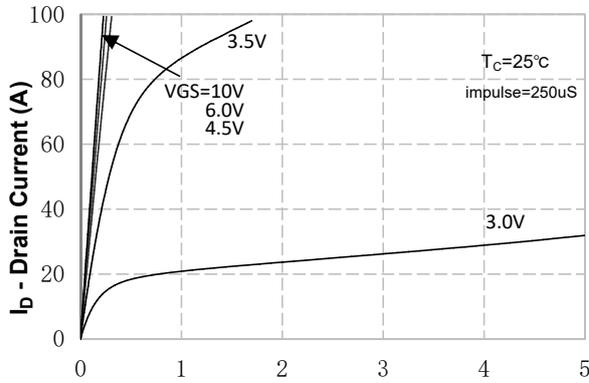


Figure 1. On-Region Characteristics

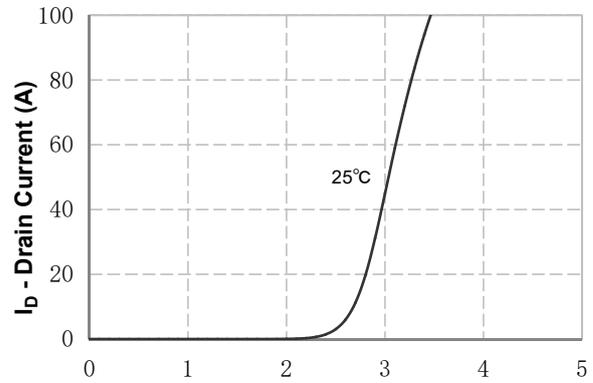


Figure 2. Transfer Characteristics

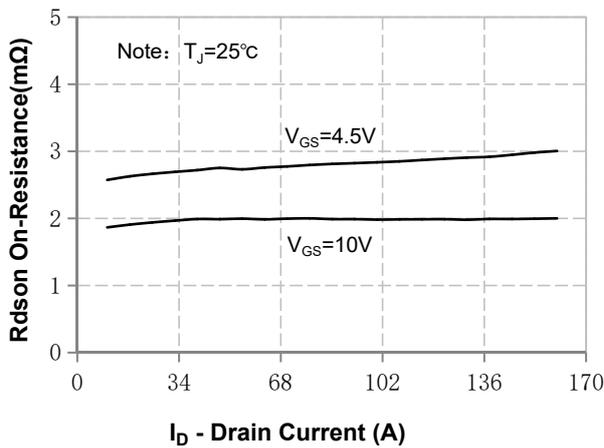


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

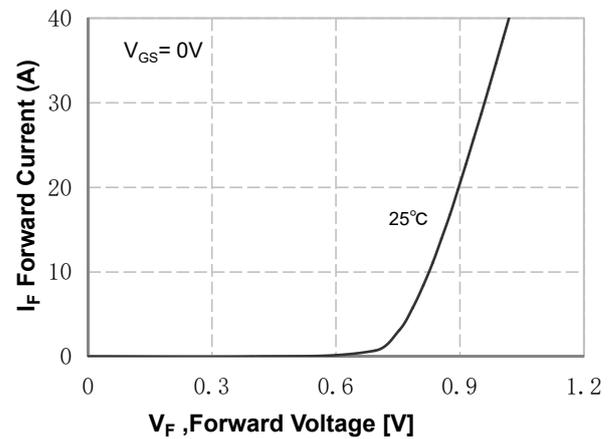


Figure 4. Body Diode Forward Voltage Variation vs Source Current

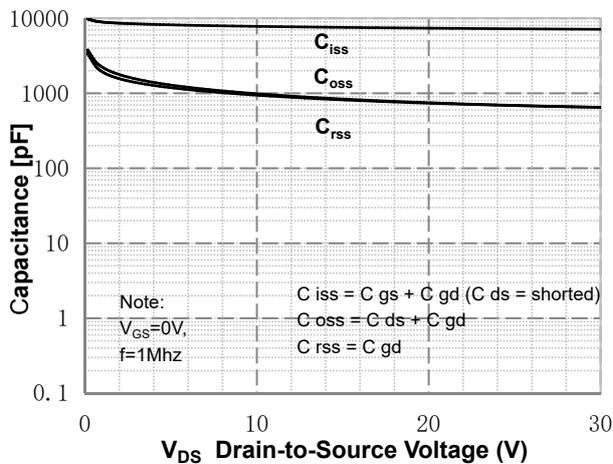


Figure 5. Capacitance Characteristics

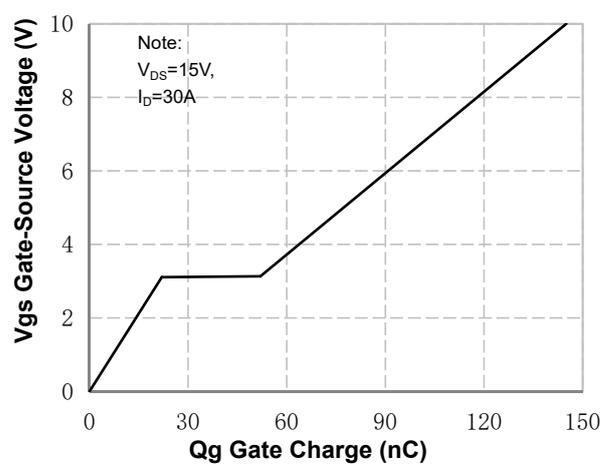


Figure 6. Gate Charge Characteristics

N- Channel Typical Characteristics (Continued)

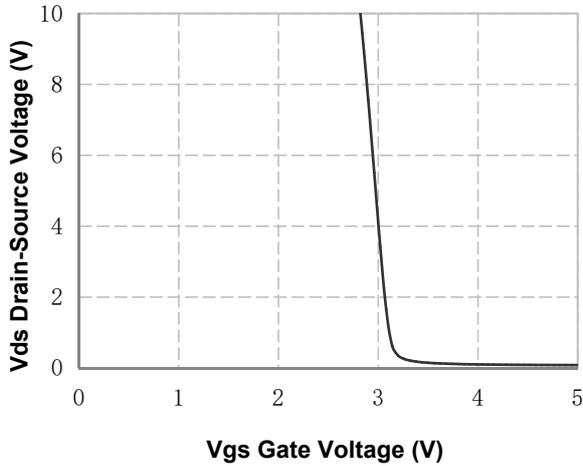


Figure 7. Vds Drain-Source Voltage vs Gate Voltage

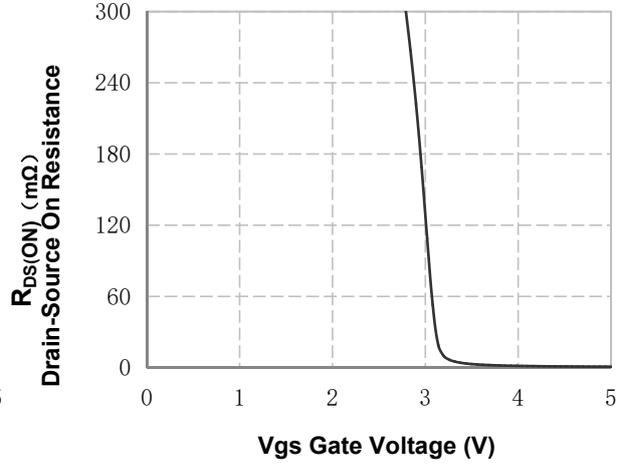


Figure 8. On-Resistance vs Gate Voltage

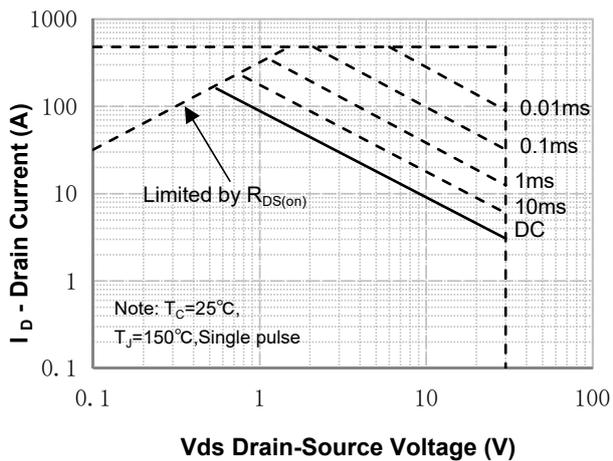


Figure 9. Maximum Safe Operating Area

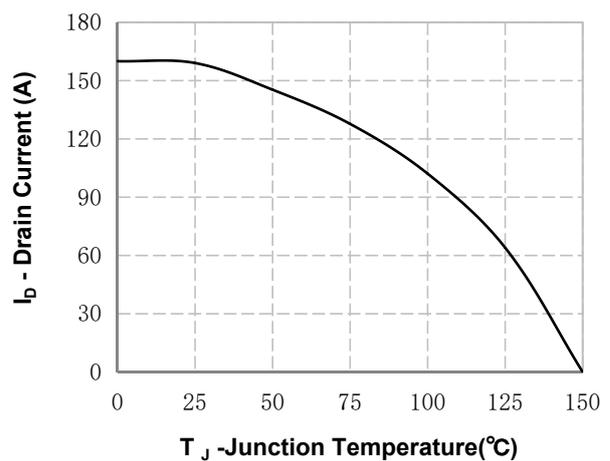


Figure 10. Maximum Continuous Drain Current vs Temperature

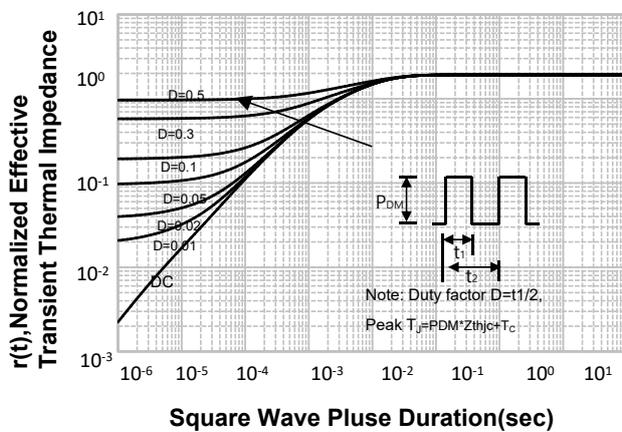
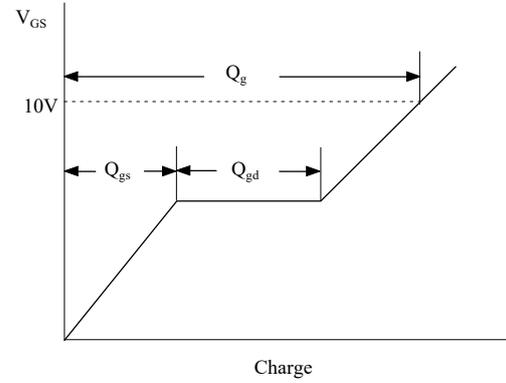
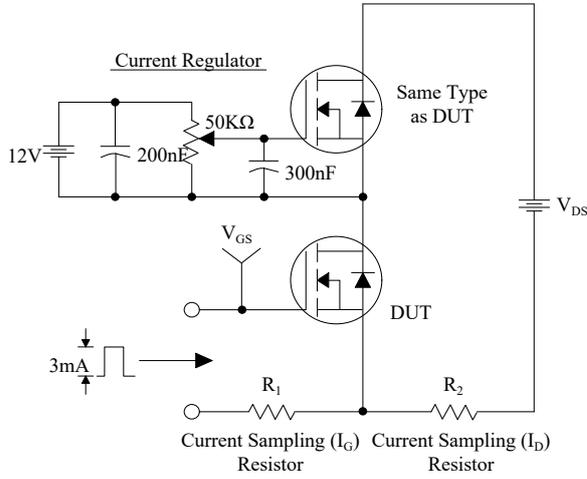
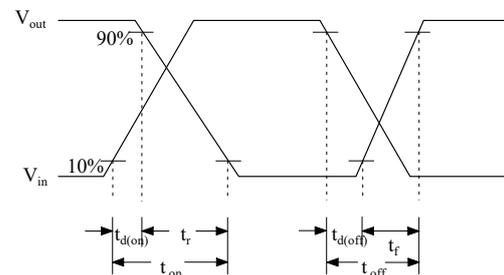
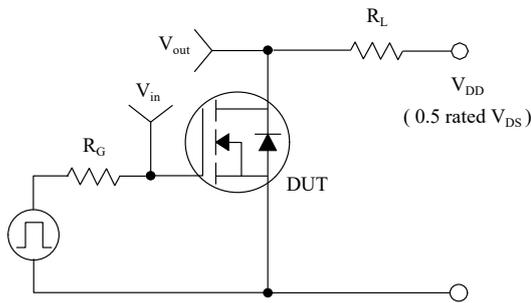


Figure 11. Transient Thermal Response Curve

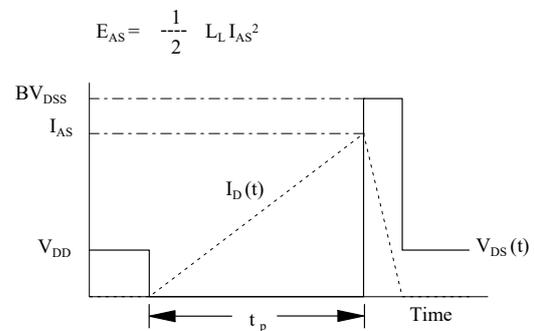
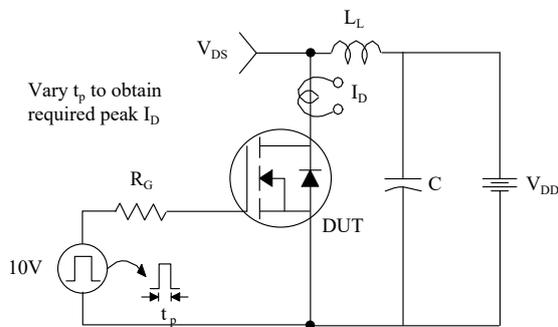
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

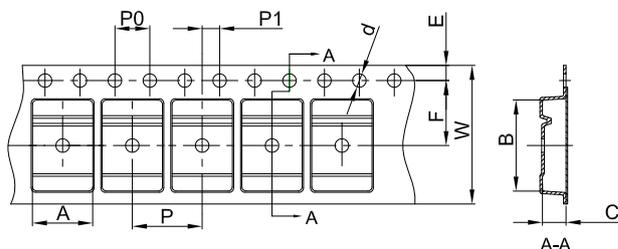


Unclamped Inductive Switching Test Circuit & Waveforms



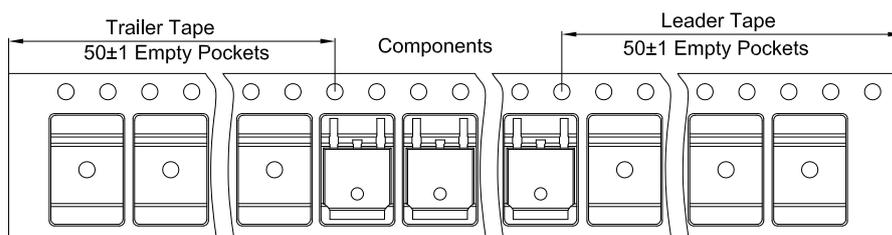
TO-252-2L Tape and Reel

TO-252 Embossed Carrier Tape

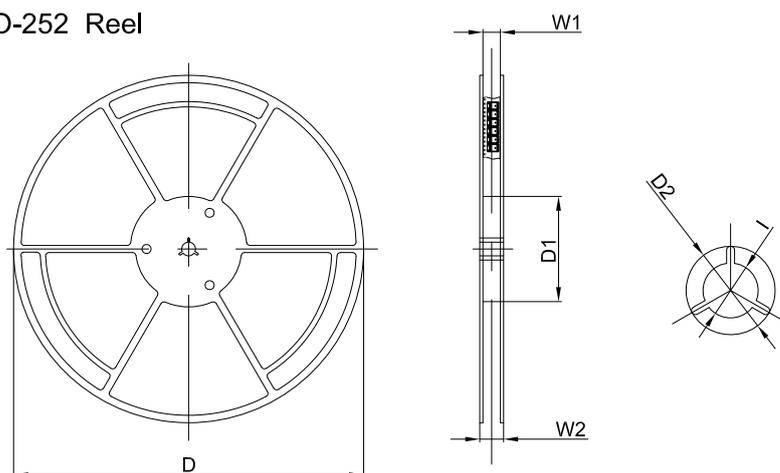


Dimensions are in millimeter										
Pkg type	A	B	C	d	E	F	P0	P	P1	W
TO-252	6.90	10.50	2.70	Ø1.55	1.75	7.50	4.00	8.00	2.00	16.00

TO-252 Tape Leader and Trailer

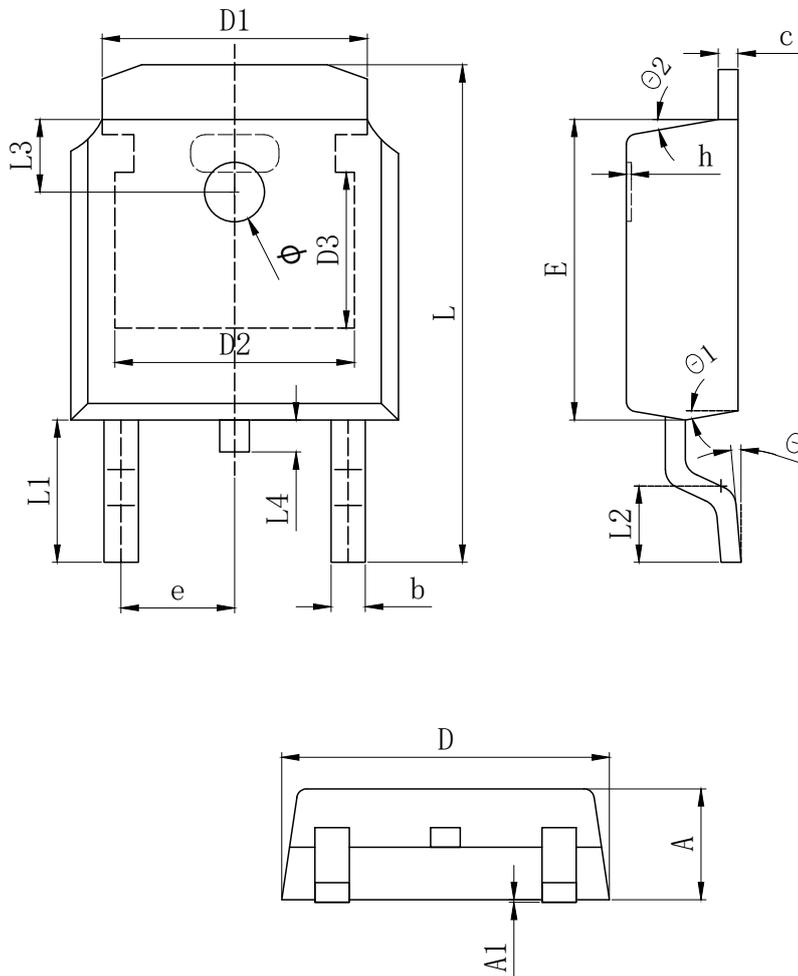


TO-252 Reel



Dimensions are in millimeter						
Reel Option	D	D1	D2	W1	W2	I
13"Dia	330.00	100.00	Ø21.00	16.40	21.00	Ø13.00

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)	G.W.(kg)
2,500 pcs	13inch	2,500 pcs	340×336×29	25,000 pcs	353×346×365	



SYMBOL	MILLIMETER		
	MIN	Typ.	MAX
A	2.200	2.300	2.400
A1	0.000		0.127
b	0.640	0.690	0.740
c (电镀后)	0.460	0.520	0.580
D	6.500	6.600	6.700
D1	5.334 REF		
D2	4.826 REF		
D3	3.166 REF		
E	6.000	6.100	6.200
e	2.286 TYP		
h	0.000	0.100	0.200
L	9.900	10.100	10.300
L1	2.888 REF		
L2	1.400	1.550	1.700
L3	1.600 REF		
L4	0.600	0.800	1.000
Φ	1.100	1.200	1.300
θ	0°		8°
θ 1	9° TYP		
θ 2	9° TYP		

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