

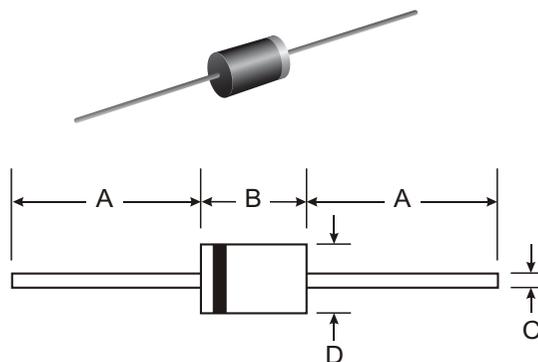
VOLTAGE RANGE: 50 - 600V
CURRENT: 1.0 A

Features

- Diffused Junction
- Low Forward Voltage Drop
- High Current Capability
- High Reliability
- High Surge Current Capability

Mechanical Data

- Case: DO - 41 Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.34 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



DO-41		
Dim	Min	Max
A	25.40	—
B	4.06	5.21
C	0.71	0.864
D	2.00	2.72
All Dimensions in mm		

Maximum Ratings and Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	1N4933	1N4934	1N4935	1N4936	1N4937	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	50	100	200	400	600	V
RMS Reverse Voltage	$V_{R(RMS)}$	35	70	140	280	420	V
Average Rectified Output Current (Note 1) $@T_A = 55^\circ\text{C}$	I_o	1.0					A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	30					A
Forward Voltage $@I_F = 1.0\text{A}$	V_{FM}	1.2					V
Peak Reverse Current $@T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage $@T_A = 100^\circ\text{C}$	I_{RM}	5.0 100					μA
Reverse Recovery Time (Note 2)	t_{rr}	200					nS
Typical Junction Capacitance (Note 3)	C_j	15					pF
Operating Temperature Range	T_j	-65 to +125					$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-65 to +150					$^\circ\text{C}$

*Glass passivated forms are available upon request

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case

2. Measured with $I_F = 0.5\text{A}$, $I_R = 1.0\text{A}$, $IRR = 0.25\text{A}$. See figure 5.

3. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

RATING AND CHARACTERISTIC CURVES 1N4933 THRU 1N4937

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

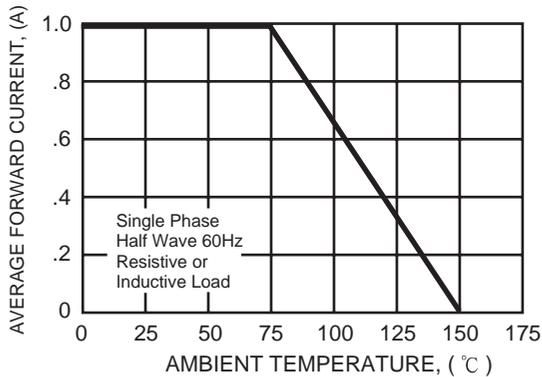


FIG. 2 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

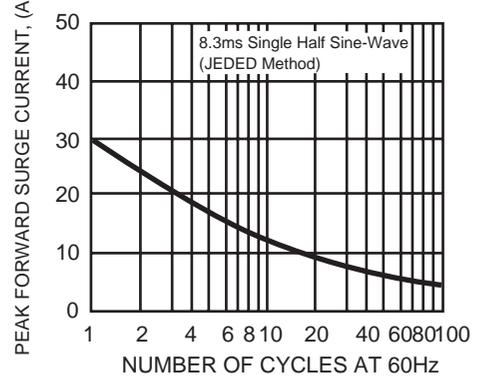


FIG. 3 - TYPICAL JUNCTION CAPACITANCE

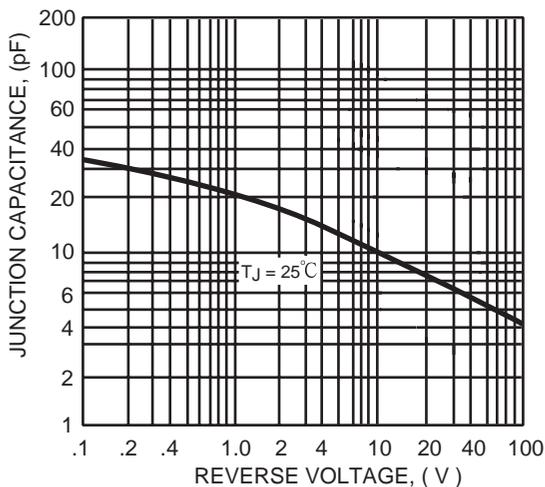


FIG. 4 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

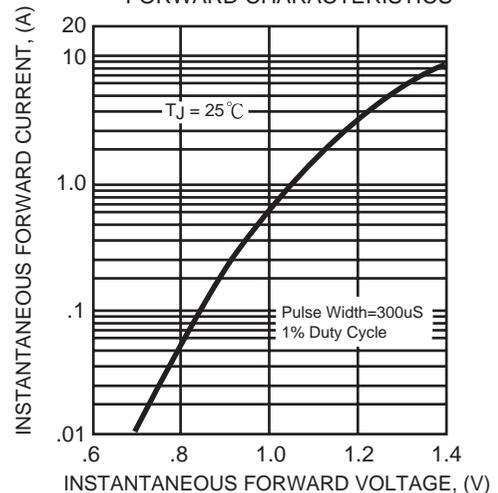


FIG. 5 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC

